

Bundesamt für Sicherheit in der Informationstechnik

# **Assurance Continuity Maintenance Report**

BSI-DSZ-CC-0639-2010-MA-01

Samsung S3CT9KC/S3CT9K9 16-bit RISC Microcontroller for Smart Card, Revision 0 with optional secure RSA/ECC V1.0 Library including specific IC Dedicated Software

from

Samsung Electronics



Common Criteria Recognition Arrangement for components up to EAL 4

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements,* version 1.0, February 2004 and the developers Impact Analysis Report (IAR). The baseline for this assessment was the Certification Report, the Security Target and the Evaluation Technical Report of the product certified by the Federal Office for Information Security (BSI) under BSI-DSZ-CC-0639-2010.

The change to the certified product is at the level of a different configuration setting by blocking of the EEPROM size, a change that has no effect on assurance. The identification of the maintained product is indicated by a new version number compared to the certified product.

Consideration of the nature of the change leads to the conclusion that it is classified as a <u>minor change</u> and that certificate maintenance is the correct path to continuity of assurance.

Therefore, the assurance as outlined in the Certification Report BSI-DSZ-CC-0639-2010 is maintained for this version of the product. Details can be found on the following pages.

This report is an addendum to the Certification Report BSI-DSZ-CC-0639-2010.

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### Assessment

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements* [1] and the Impact Analysis Report (IAR) [2]. The baseline for this assessment was the Certification Report of the certified product (Target of Evaluation, TOE) [3], the Security Target [4] and the Evaluation Technical Report as outlined in [5].

The vendor for the Samsung S3CT9KC/S3CT9K9 16-bit RISC Microcontroller for Smart Card, Revision 0 with optional secure RSA/ECC V1.0 Library including specific IC Dedicated Software, Samsung Electronics, submitted an IAR [2] to the BSI for approval. The IAR is intended to satisfy the requirements outlined in the document *Assurance Continuity: CCRA Requirements* [1]. In accordance with those requirements, the IAR describes (i) the changes made to the certified TOE, (ii) the evidence updated as a result of the changes and (iii) the security impact of the changes.

The Samsung S3CT9KC/S3CT9K9 16-bit RISC Microcontroller for Smart Card, Revision 0 with optional secure RSA/ECC V1.0 Library including specific IC Dedicated Software due to a reduction of the EEPROM size. The EEPROM size of the the product S3T9KC is blocked from 144KB to 80KB and the product S3CT9K9 from 144KB to 36KB. The products S3CT9KW, S3CT9KC and S3CT9K9 have the same physical EEPROM size. Configuration Management procedures required a change in the version number from S3CT9KW Revision 0 to S3CT9KC Revision 0 and from S3T9KW Revision 0 to S3CT9K9 Revision 0. The device type for S3CT9KC, Revision 0 is identified by 140CH. The device type for S3CT9K9, Revision 0 is identified by 1409H. This information is stored in the EEPROM and can be read out by the user of the card via the normal EEPROM read command.

## Conclusion

The change to the TOE is at the level of different configuration setting by blocking of the EEPROM size, a change that has no effect on assurance. Examination of the evidence indicates that the change performed is limited to the Configuration Management Documentation [7] of the TOE. Security Target Lite [6] was editorially updated. Consideration of the nature of the change leads to the conclusion that it is classified as a minor change and that certificate maintenance is the correct path to continuity of assurance.

Therefore, BSI agrees that the assurance as outlined in the Certification Report [3] is maintained for this version of the product.

#### Additional obligations and notes for the usage of the product:

All aspects of assumptions, threats and policies as outlined in the Security Target not covered by the TOE itself need to be fulfilled by the operational environment of the TOE.

The customer or user of the product shall consider the results of the certification within his system risk management process. In order for the evolution of attack methods and techniques to be covered, he should define the period of time until a re-assessment for the TOE is required and thus requested from the sponsor of the certificate.

Some security measures are partly implemented in the hardware and require additional configuration or control or measures to be implemented by the IC Dedicated Support Software or Embedded Software.

For this reason the TOE includes guidance documentation which contains guidelines for the developer of the IC Dedicated Support Software and Embedded Software on how to securely use the microcontroller chip and which measures have to be implemented in the software in order to fulfil the security requirements of the Security Target of the TOE.

In the course of the evaluation of the composite product or system it must be examined if the required measures have been correct and effectively implemented by the software. Additionally, the evaluation of the composite product or system must also consider the evaluation results as outlined in the document ETR for composite evaluation [8].

According to the scheme rules, evaluation results outlined in the document ETR for composition as listed above can usually be used for composite evaluations building on top, as long as the ETR for composition document is not older than one year and an attack assumed to be not feasible within the scope of these evaluations has not been performed successfully.

Additional Note: The strength of the cryptographic algorithms was not rated in the course of the product certification and this maintenance procedure (see BSIG Section 9, Para. 4, Clause 2).

This report is an addendum to the Certification Report [3].

#### References

- [1] Common Criteria document CCIMB-2004-02-009 "Assurance Continuity: CCRA Requirements", version 1.0, February 2004
- [2] Security Impact Analysis Report, S3CT9KW and S3CT9KC/K9 Comparison Version 1.0 issued on 28th July 2010, Samsung Electronics (confidential document)
- [3] Certification Report BSI-DSZ-CC-0639-2010 for Samsung S3CT9KW 16bit RISC Microcontroller for Smart Card, Revision 0 with optional secure RSA/ECC V1.0 Library including specific IC Dedicated Software Software, Samsung Electronics Co., Ltd. Bundesamt für Sicherheit in der Informationstechnik, 17 August 2010.
- [4] Security Target of S3CT9KW 16-bit RISC Microcontroller for Smart Cards with optional Secure RSA and ECCLibrary – Project Crow, Version 1.6, 2010-03-10, Samsung Electronics (confidential document)
- [5] Evaluation Technical Report Summary (ETR Summary), BSI-DSZ-CC-0639, S3CT9KW, Version 1, 2010-07-05, TÜViT (confidential document) TÜViT (confidential document)
- [6] Security Target Lite of Samsung S3CT9KC/S3CT9K9 16-bit RISC Microcontroller for Smart Card with optional Secure RSA and ECC Library including specific IC Dedicated Software, Version 1.0, 3th September 2010
- [7] Life Cycle Definition (Class ALC\_CMC.4/CMS.5) Version 2.0 Issued on 28th July 2010, Samsung Electronics (confidential document)